The op-amp is ideal, with $V_{CC} = 10\,\text{V}$ and $V_{EE} = -10\,\text{V}$. The diode forward voltage, $V_D = 0.7\,\text{V}$.

- What is the frequency of oscillation.
- Sketch $V_o$ when the oscillation amplitude has stabilized.
- Indicate the approximate voltage of oscillation on the sketch.
The op-amp is ideal, with $V_{CC} = 2 \, \text{V}$ and $V_{EE} = -2 \, \text{V}$.

Initial conditions are: $V_- = 0$ and $V_o = +V_{CC}$.

Sketch as a function of time: 1) $V_-$, 2) $V_+$, 3) $V_o$. 

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Network Diagram:
- 73 kΩ resistor from ground to the non-inverting input.
- 38 kΩ resistor from the inverting input to the output.
- 39 nF capacitor from the inverting input to ground.
- 28 kΩ resistor from the inverting input to ground.
Initial conditions are that the charge on the capacitor is zero. $V_{CC} = 9 \text{ V}$.

- Sketch $V_o$, $V_A$ and $V_B$.
- What is the length of the $V_o = \text{high}$ and $V_o = \text{low}$ outputs?