

Lecture 21

Nov. 25, 2016

Binary \rightarrow M-ary
Efficient Manner

- (- TX design)
 - (- RX design)
- \rightarrow Performance

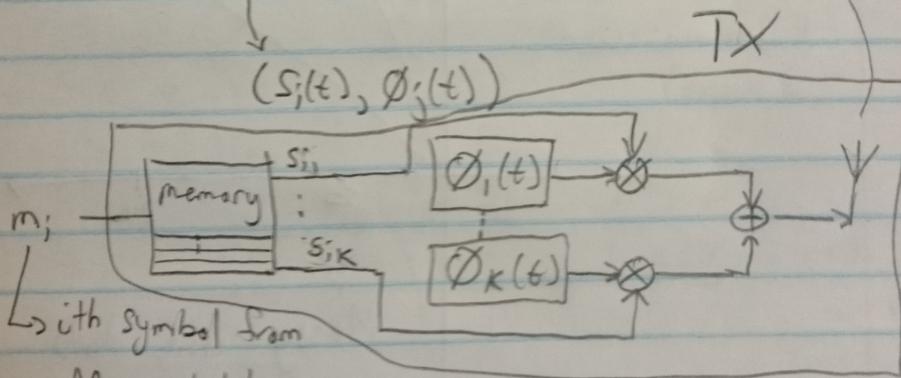
$$\{m_i\}_{i=1}^M \rightarrow \{s_i(t)\}_{i=1}^M$$

$\{\phi_j\}_{j=1}^K$: basis functions
 $k \leq M$ often $k \ll M$

Ex: m-QAM $k=2$

$$s_i(t) = \sum_{j=1}^k s_{ij} \phi_j(t)$$

$i=1, \dots, M$
 $j=1, \dots, k$



m_i with symbol from
M possibilities
 $(\log_2 M \text{ bits of info})$

Size of memory $M \times K$

Ex: 1024 QAM

$$S_i(t) = A_i \cos(2\pi f_c t + \theta_i), \quad i=1, \dots, 1024$$

$$\phi_1(t) = \sqrt{\frac{2}{T}} \cos 2\pi f_c t$$

$$\phi_2(t) = \sqrt{\frac{2}{T}} \sin 2\pi f_c t$$

$$M=2$$

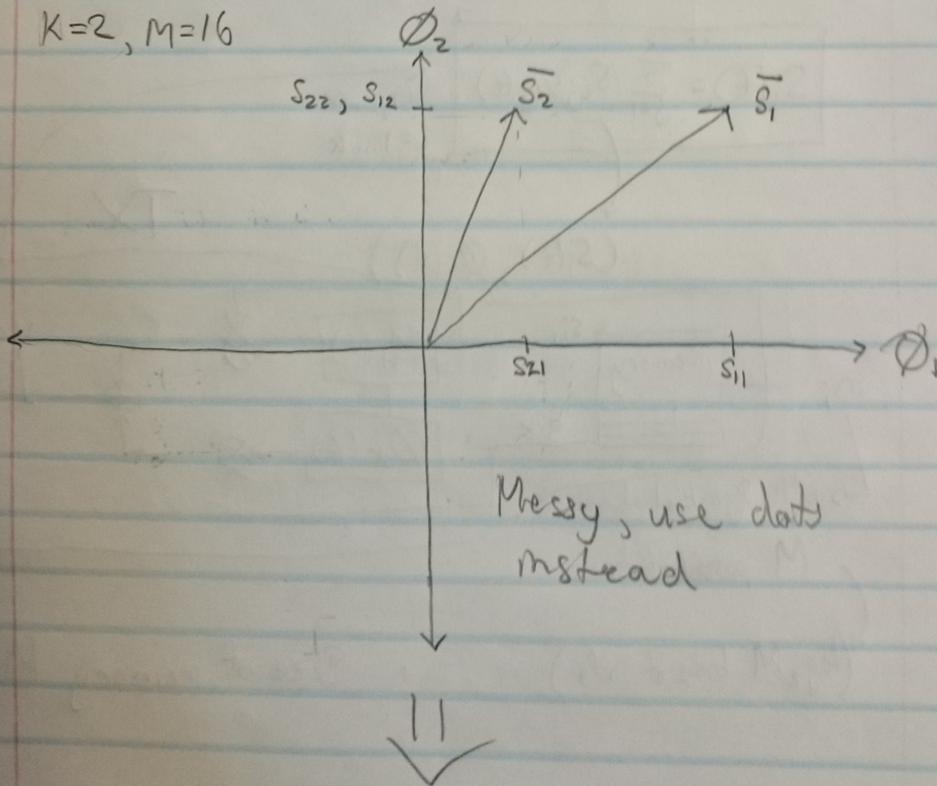
$$S_i(t) = S_{i1} \phi_1(t) + S_{i2} \phi_2(t), \quad i=1, \dots, 1024$$

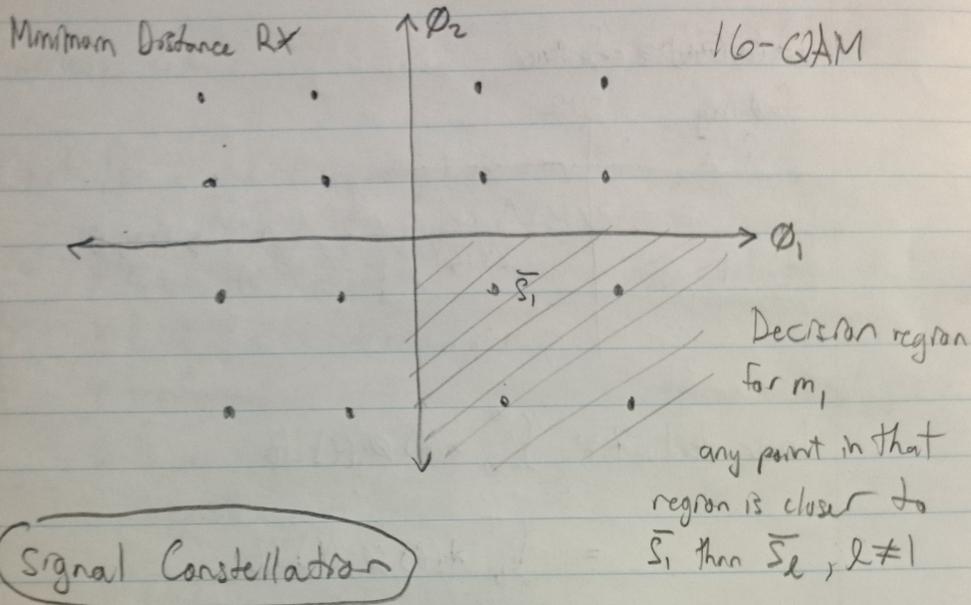
$$1024 \times 2 = 2048$$

$$M \times K$$

Ex:

$$K=2, M=16$$



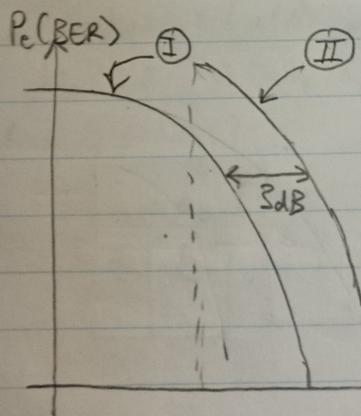


Set of all the possible signal points (vectors)

Antipodal

(I) $P_{e,I} = \frac{1}{2} \text{erfc}\left(\sqrt{\frac{E}{N_0}}\right)$

(II) $P_{e,II} = \frac{1}{2} \text{erfc}\left(\sqrt{\frac{E}{2N_0}}\right)$



AWGN: waterfall
(exponential decrease)

$$P_{e,I} = P_{e,II}$$

$$\frac{1}{2} \text{erfc}\left(\sqrt{\frac{E}{N_0}}\right)_I = \frac{1}{2} \text{erfc}\left(\sqrt{\frac{E}{2N_0}}\right)_{II}$$

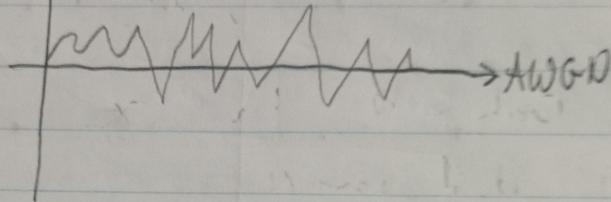
$$\left(\sqrt{\frac{E}{N_0}}\right)_I = \left(\sqrt{\frac{E}{2N_0}}\right)_{II}$$

$$2\left(\frac{E}{N_0}\right)_I = \left(\frac{E}{N_0}\right)_{II}$$

AWGN \rightarrow exp dec

fading

SNR



$$\text{Inner Product} = \int_{-\infty}^{\infty} x(t) \phi_j(t) dt$$

$$= \int_0^T x(t) \phi_j(t) dt$$